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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10076055	FILING DATE 02/14/2002	CLASS 257	SUBCLASS 347	GAU 2811	EXAMINER T. TRAN
<b>**APPLICANTS:</b> Takizawa Teruo;					
<b>**CONTINUING DATA VERIFIED:</b>  NONE TT					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-39511 02/16/2001 YES TT					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 110 conditions met				9319S-000329	
Verified and Acknowledged Examiners's initials		TT			
TITLE : Semiconductor device and method for manufacturing semiconductor device					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
Assistant Examiner		<b>DRAWING</b>	
		Sheet Draw.	Fig. Draw.
Primary Examiner		Print Fig.	
<input type="checkbox"/> <b>TERMINAL</b> <b>DISCLAIMER</b>		Application Examiner	
<b>PREPARED FOR ISSUE</b>			
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